**How FastHenry + ACA is used:**

Initially the program asks the user to choose between first and second nearest neighbour as documented in the paper [AA].

The user is then asked to specify the ACA tolerance, note that the SVD tolerance is taken as 10 times smaller [AA].

Then the user is requested to specify a GMRES tolerance. A tolerance of 10^(-8) was used in [AA].

The direct memory and MLACA-SVD memory as well as total memory is then presented to the user once the direct and approximated portion of the inductance matrix has been computed.

The final port currents are stored in ix.cirr.

[AA] B. A. P. Nel and M. M. Botha, “An efficient MLACA-SVD solver for superconducting integrated circuit analysis,” IEEE Transactions on Applied Superconductivity, 2018, submitted for publication.